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08791 7590 04/22/2005

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07/27/2005 WASFAW2 00000134 10750243

01 FC:1501 1400.00 OP
02 FC:1504 300.00 OP
03 FC:1001 30.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,243	12/31/2003	Se-Jun Kim	51876P556	8888

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Eric Flynn	(Depositor's name)
<i>Eric Flynn</i>	(Signature)
7-22-2005	(Date)

TITLE OF INVENTION: ANALOG DELAY LOCKED LOOP HAVING DUTY CYCLE CORRECTION CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/22/2005
EXAMINER		ART UNIT	CLASS-SUBCLASS		
COX, CASSANDRA F		2816	327-158000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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(A) NAME OF ASSIGNEE

Hynix Semiconductor Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kyoungki-Do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Typed or printed name Eric S. Hyman

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